



St. MARTIN'S ENGINEERING COLLEGE

An Autonomous Institute
 Dhulapally, Secunderabad – 500100
 NBA & NAAC A+ Accredited



Department of Electronics & Communication Engineering

TIME – TABLE

Year : II -B. Tech
 Academic Year: 2020-2021

Semester : I
 W.e. f: 3/11/2020

For All Lateral Entry Students
 Mode of Teaching: Microsoft Teams

HOUR	1:00 PM to 1:40 PM	1:40 PM to 2:20 PM	2:30 PM To 3:10 PM	3.10 PM to 3:50 PM
DAY	1	2	3	4
MON	PTSP NV	SS CK	DSD SPM	NATL CHN
TUE	DSD SPM	NATL CHN	SS CK	EDC BR
WED	SS CK	PTSP NV	DSD SPM	NATL CHN
THU	EDC BR	SS CK	PTSP NV	NATL CHN
FRI	NATL CHN	PTSP NV	DSD SPM	EDC BR
SAT	EDC BR	SS CK	PTSP NV	COI KS

SS	:	Signals and Systems	CK	DR. CHAITANYA KRISHNA
PTSP	:	Probability Theory and Stochastic Processes	NV	N. VISWANATH
DSD	:	Digital System Design	MT	S.P.MANIKANTA
EDC	:	Electronic Devices and Circuits	RCB	B. RAVICHANDER
NATL	:	Network Analysis and Transmission Lines	CHN	CH. NIROSHA
COI	:	Constitution of India	KS	K.SUDHA

CHIEF IN-CHARGE (TIME TABLE)	HOD-ECE	PRINCIPAL

Copy to: Principal, Chief in Charge [Time Tables], HOD's - EEE, CSE, CIVIL, ECE, IT, S & H, MBA